

Fig. 1

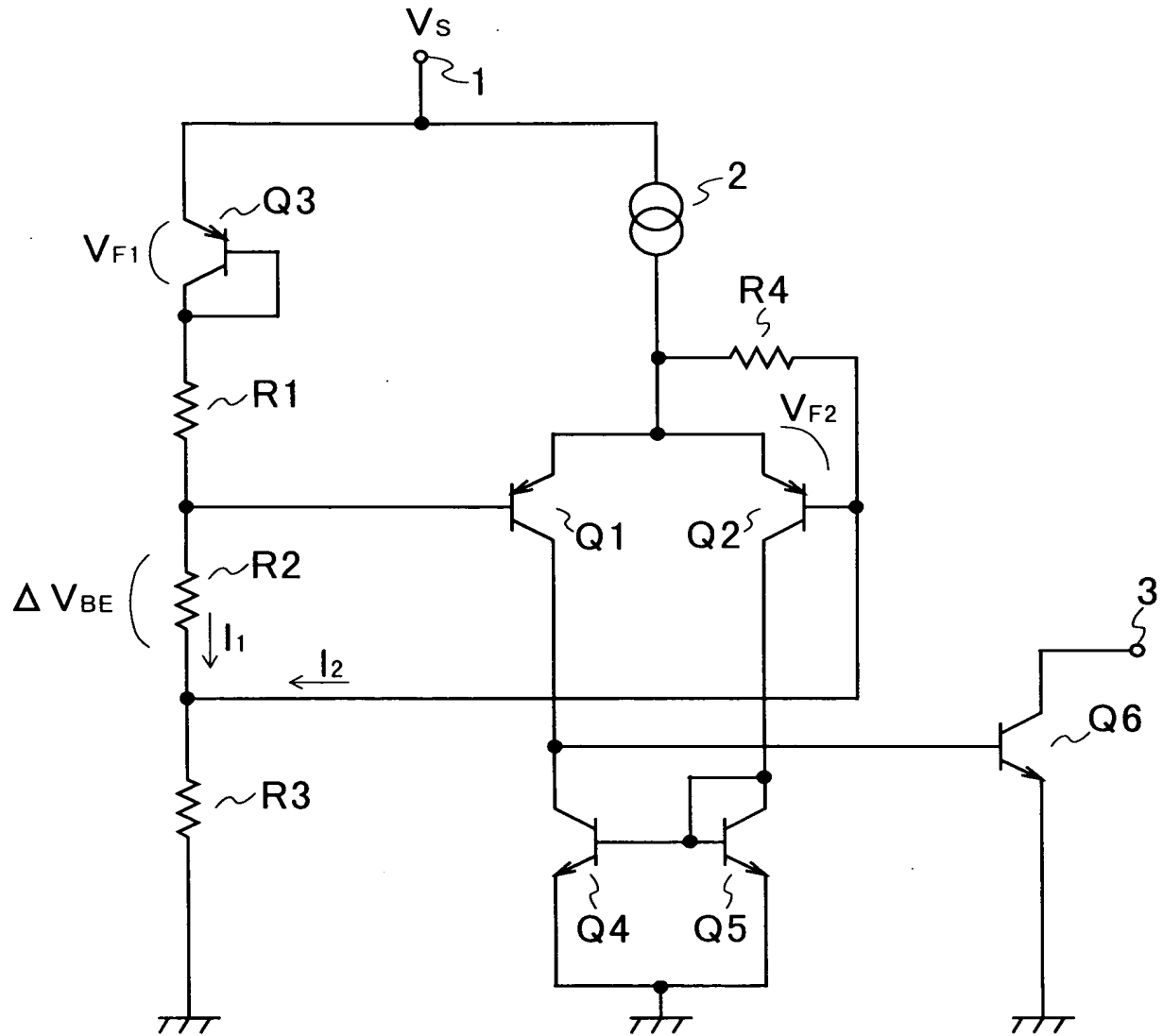


Fig. 2

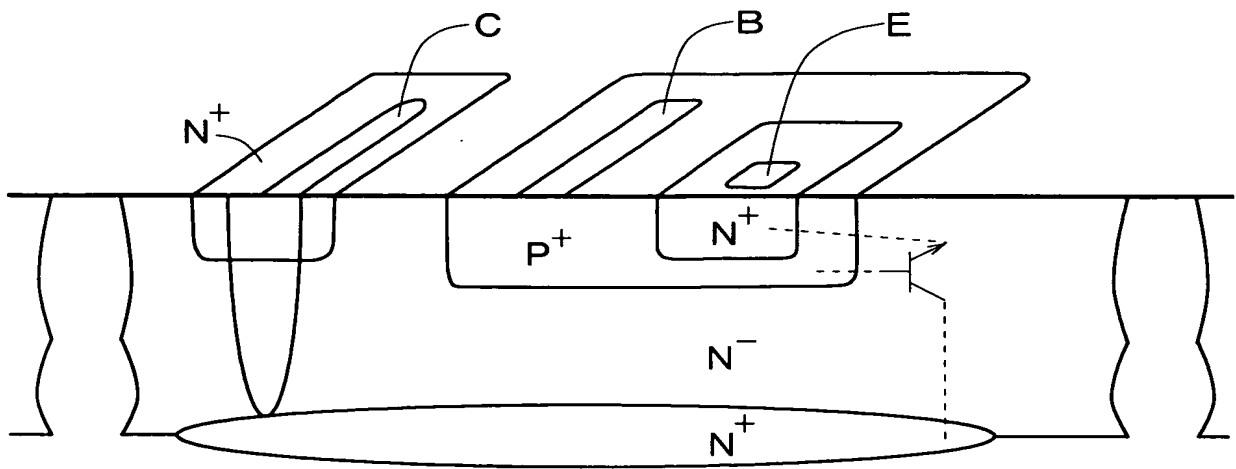
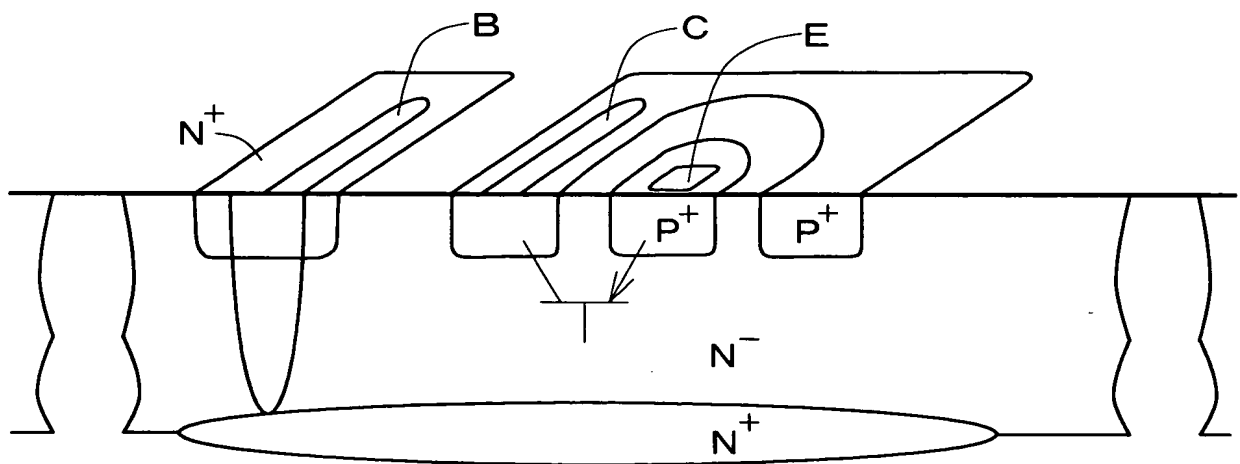


Fig. 3



Title: VOLTAGE DETECTION CIRCUIT  
Inventor: HIRAMATSU, et al.  
Appln. No.: Unknown  
Docket No.: 103213-00076

Prior Art  
F i g . 4

